



**DV CLUB VIETNAM**

# **HIGH PERFORMANCE COMPUTING AND AUTOMOTIVE**

**16 DECEMBER 2025**

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**FCE**

VNUHCM-University of Information Technology  
**Faculty of Computer Engineering**

# **ATTENDEES**

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**Total Registrations: 166**

**In-person: 42**

**Remote: 124**

# AGENDA

TIME	ACTIVITY
12.00	Arrival, registration, networking
13.00	<b>Semiconductor Education in VNUHCM-UIT: A case study with Design Verification Course</b> by Minh Son NGUYEN, Vietnam National University at HCMCity – University of Information
13.30	<b>Agentic AI for Next-Generation Chip Verification: Verisium Powered by JedAI</b> by Anika Sunda, Cadence Vietnam
14.00	<b>Enhancing Verification Efficiency with AutoFocus on the Verisium Platform</b> by Nguyen Hoang Nghia, Renesas Design Vietnam
14.30	<b>Marvell's Accelerated Infrastructure for AI Data Center</b> by Quang-Dam Le, Marvell Technology Vietnam
15.00	Break with refreshments/networking
15.30	<b>Unified Test Vector Framework Across Multiple Testbench Levels and Software Validation</b> by Hung Le, BOS Semiconductors Vietnam
16:30	<b>Practical experience sharing of adopting Jasper on register/connection verification</b> by Richard An, Realtek Semiconductor Corp.
17:00	<b>Using AI in Verification</b> by Mike Bartley, Alpinum
17.30	END   Refreshments & Pizza/networking



**THANK YOU!**

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